

- ☐ Drafts
- ☐ Pending
- ☒ Active
 - ☒ L1: (8744) ferroelectric adj2 memory
 - ☒ L2: (674316) register
 - ☒ L3: (9010) programming adj3 code
 - ☒ L4: (297190) reference adj2 (voltage value)
 - ☒ L5: (1745210) test testing
 - ☒ L6: (130) 2 with 3
 - ☒ L7: (339) 1 with 4
 - ☒ L9: (3) 6 and 7 and 4
 - ☒ L11: (339) 7 and 4
 - ☒ L12: (1) 6 and 7 and 5
 - ☒ L13: (17) 2 and 7 and 5
- ☐ Failed
- ☒ Saved
- ☐ Favorites
- ☐ Tagged (0)
- ☐ UDC
- ☐ Queue
- ☒ Trash
 - ☒ L10: (1) 6.clm. and 7.clm. and 4.clm.
 - ☒ L8: (1) 6 same 7 same 4

Search List Browse Queue Clear

DBs: US:PGPUB:USPAT:EPO:JPD:D ☒ Plurals

Default operator: OR

☒ Highlight all hit terms initially

2 and 7 and 5

EAST Search History Printout

10/17/06

HMH

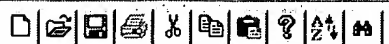
BRS form IS&R form Image Text HTML

	U	1	Document ID	Title	Current OR	Current XRef	Retri.
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20050276089	Plateline voltage pulsing to red	365/100		
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20050052895	FeRAM using programmable registe	365/145		
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20040233744	Ferroelectric memory with wide o	365/200		
4	<input type="checkbox"/>	<input type="checkbox"/>	US 20040174750	Programmable reference for 1T/1C	365/189.09		

Hits Details HTML

Ready

CAP NUM



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 - ☒ L11: (339) 7 and 4
 - ☒ L12: (1) 6 and 7 and 5
 - ☒ L13: (17) 2 and 7 and 5
 - ☒ L14: (2) 2.clm. and 7.clm. and 5.clm.
- ☐ Failed
- ☒ Saved
- ☒ Favorites
- ☒ Tagged (0)
- ☒ UDC
- ☒ Queue
- ☒ Trash

Search List Browse Queue Clear

DBs US:PGPUB:USPAT:EPO:JPD:D ☒ Plurals

Default operator: OR

☒ Highlight all hit terms initially

2.clm. and 7.clm. and 5.clm.

PG - Pub Search History
Printout
10/17/06
HW

BRS form IS&R form Image Text HTML

	U	1	Document ID	Title	Current OR	Current XRef	Retrieval
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20040158773	Test mode control device using n	714/30		
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20040032759	Memory device, circuits and meth	365/145		

Hits Details HTML

Ready

NUM

12/09/2003	SERIAL BUS CONTROLLER USING NONVOLATILE FERROELECTRIC MEMORY	KANG, HEE BOK	<i>Printout for the inventor</i>		
10730134	Not Issued	71	12/09/2003	Test mode control device using nonvolatile ferroelectric memory	KANG, HEE BOK
10730985	<u>6944092</u>	150	12/10/2003	SEMICONDUCTOR MEMORY DEVICE	KANG, HEE BOK
10731099	<u>6850447</u>	150	12/10/2003	NONVOLATILE FERROELECTRIC MEMORY DEVICE HAVING MULTI-BIT CONTROL FUNCTION	KANG, HEE BOK
10734130	<u>7081779</u>	150	12/15/2003	RESET SIGNAL GENERATING CIRCUIT	KANG, HEE BOK
10734140	<u>6954370</u>	150	12/15/2003	NONVOLATILE FERROELECTRIC MEMORY DEVICE	KANG, HEE BOK
10734168	<u>6977835</u>	150	12/15/2003	FERROELECTRIC REGISTER, AND METHOD FOR MANUFACTURING CAPACITOR OF THE SAME	KANG, HEE BOK
10734313	<u>7057970</u>	150	12/15/2003	NONVOLATILE FERROELECTRIC MEMORY AND CONTROL DEVICE USING THE SAME	KANG, HEE BOK
10737757	<u>6992912</u>	150	12/18/2003	NONVOLATILE FERROELECTRIC MEMORY DEVICE HAVING TIMING REFERENCE CONTROL FUNCTION AND METHOD FOR CONTROLLING THE SAME	KANG, HEE BOK
10737758	Not Issued	41	12/18/2003	Nonvolatile programmable logic circuit	KANG, HEE BOK
10737833	<u>6982896</u>	150	12/18/2003	NONVOLATILE FERROELECTRIC MEMORY DEVICE HAVING A MULTI-BIT CONTROL FUNCTION	KANG, HEE BOK
10737845	<u>6956760</u>	150	12/18/2003	FERROELECTRIC RANDOM ACCESS MEMORY	KANG, HEE BOK
10737846	<u>6867998</u>	150	12/18/2003	CELL ARRAY BLOCK OF FERAM, AND FERAM USING CELL ARRAY	KANG, HEE BOK
10738088	Not Issued	41	12/18/2003	Input/output byte control device using nonvolatile ferroelectric register	KANG, HEE BOK
10740590	<u>7075810</u>	150	12/22/2003	NONVOLATILE FERROELECTRIC MEMORY DEVICE	KANG, HEE BOK
10740891	<u>6922352</u>	150	12/22/2003	FERAM HAVING TEST CIRCUIT AND METHOD FOR TESTING THE SAME	KANG, HEE BOK
10740892	<u>6924998</u>	150	12/22/2003	FERAM USING PROGRAMMABLE REGISTER	KANG, HEE BOK
10742394	<u>7012828</u>	150	12/22/2003	DATA CONTROL DEVICE USING A NONVOLATILE FERROELECTRIC MEMORY	KANG, HEE BOK
10742395	<u>6870785</u>	150	12/22/2003	NONVOLATILE FERROELECTRIC MEMORY DEVICE HAVING MULTI-BIT CONTROL FUNCTION	KANG, HEE BOK